



PROCUREMENT MANAGEMENT UNIT (PMU)

Taft Ave. cor. Ayala Blvd., Ermita, Manila

(+632) 5317-1768 loc. 738

pmu@pnu.edu.ph

Subject: **BID BULLETIN No. 1**

PROJECT: **Supply, Delivery, Installation, and Configuration of HyperConverged Infrastructure (HCI) for the University Data Center**

Date: **August 22, 2025**

This is to inform all the Potential Bidder/s of the following agreements/changes during the Pre-Bid Conference held on August 22, 2025 Via Zoom.

1. Section VII. Technical Specifications

FROM	TO
<p>Hardware Technical Specifications: The offered brand and model must meet or exceed the following minimum technical specifications:</p> <ul style="list-style-type: none"> • Minimum of two (2) units of 2U rack-mounted HCI-compliant nodes. <ul style="list-style-type: none"> • Each node must include: <ul style="list-style-type: none"> • Two (2) x86-based physical processors per node (total of 4 CPUs) <ul style="list-style-type: none"> - CPU Specs: Minimum of 16 cores per CPU, 2.4GHz base frequency, 3.4GHz max frequency, 24MB cache. - Total Cores: Minimum of sixty-four (64) physical cores. • Memory: 16× 32GB DDR4 RDIMM modules (total: 512GB physical memory) • 30 • 0 Boot Drives: Two (2) × 960GB SSD SAS drives per node (RAID 1 for redundancy). • Storage Capacity: <ul style="list-style-type: none"> - 12 × 4TB 7.2K RPM SATA HDDs (total: 48TB raw storage). - Additional 4 × 960GB SSD SAS (3.5") for caching or performance tiering. • Networking Ports: <ul style="list-style-type: none"> - Four (4) × 10Gb SFP+ ports and Four (4) × 1GbE 	<p>Hardware Technical Specifications: The offered brand and model must meet or exceed the following minimum technical specifications:</p> <ul style="list-style-type: none"> • Minimum of two (2) units of 2U rack-mounted HCI-compliant nodes. <ul style="list-style-type: none"> • Two (2) x86-based physical processors per node (total of 4 CPUs) <ul style="list-style-type: none"> - CPU Specs: Minimum of 16 cores per CPU, 2.4GHz base frequency, 3.4GHz max frequency, 24MB cache. - Total Cores: Minimum of sixty-four (64) physical cores. • Memory: 16× 32GB DDR4 RDIMM modules (total: 512GB physical memory) • 30 • 0 Boot Drives: Two (2) × 960GB SSD SAS drives per node (RAID 1 for redundancy). • Storage Capacity: <ul style="list-style-type: none"> - 12 × 4TB 7.2K RPM SATA HDDs (total: 48TB raw storage). - Additional 4 × 960GB SSD SAS (3.5") for caching or performance tiering. • Networking Ports: <ul style="list-style-type: none"> - Four (4) × 10Gb SFP+ ports and Four (4) × 1GbE

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<p>ports and Four (4) × 1GbE ports per node. - Includes 8 × SFP+ 10G Optical Transceivers and necessary cables.</p> <ul style="list-style-type: none"> • RAID Controller: One (1) RAID card per node with minimum 4GB cache, supporting RAID 1 for boot drives. • Power Supply: Two (2) × 900W Version 3.0 AC hot-swappable PSUs per node (total: 4 units). • Expansion Compatibility: Fully compatible with PCIe Gen 3 and Gen 4 modules 	<p>ports per node. - Includes 8 × SFP+ 10G Optical Transceivers and necessary cables.</p> <ul style="list-style-type: none"> • RAID Controller: One (1) RAID card per node with minimum 4GB cache, supporting RAID 1 for boot drives. • Power Supply: Two (2) × 900W Version 3.0 AC hot-swappable PSUs per node (total: 4 units). • Expansion Compatibility: Fully compatible with PCIe Gen 3 and Gen 4 modules.
<p>Technical Support Service:</p> <ul style="list-style-type: none"> • Migrate existing VMs as required 	<p>Technical Support Service:</p> <ul style="list-style-type: none"> • Migrate LMS Application and Database Server to HCI

This **Supplemental Bid Bulletin No. 1** shall form part of the Bid Documents (should be included/attached in your Bidding Documents). Any provisions in the Bid Documents inconsistent herewith are hereby amended, modified, and superseded accordingly.

For the information and guidance of all concerned.


DR. TERESITA T. RUNGDUIN
Chairperson, TR-BAC

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